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Application Number:	10798911				
International Application Number:					
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Title of Invention:	Method and apparatus for improving performance margin in logic paths				
First Named Inventor/Applicant Name:	Richard Lee Donze				
Correspondence Address:	Robert R. Williams IBM Corporation, Dept. 917 3605 Highway 52 North - Rochester MN 55901-7829 US 5072532761 -				
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File Listing:

Document Number	Document Description	File Name	File Size(Bytes) /Message Digest	Multi Part /.zip	Pages (if appl.)
1 Amendment After Final	ROC920030185US1_Amend mentAF_07-11-07.pdf	513562	no	11	
		133d8536258feefee0b081dc15f38d1c3 5281fed			
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National Stage of an International Application under 35 U.S.C. 371

If a timely submission to enter the national stage of an international application is compliant with the conditions of 35 U.S.C. 371 and other applicable requirements a Form PCT/DO/EO/903 indicating acceptance of the application as a national stage submission under 35 U.S.C. 371 will be issued in addition to the Filing Receipt, in due course.

New International Application Filed with the USPTO as a Receiving Office

If a new international application is being filed and the international application includes the necessary components for an international filing date (see PCT Article 11 and MPEP 1810), a Notification of the International Application Number and of the International Filing Date (Form PCT/RO/105) will be issued in due course, subject to prescriptions concerning national security, and the date shown on this Acknowledgement Receipt will establish the international filing date of the application.